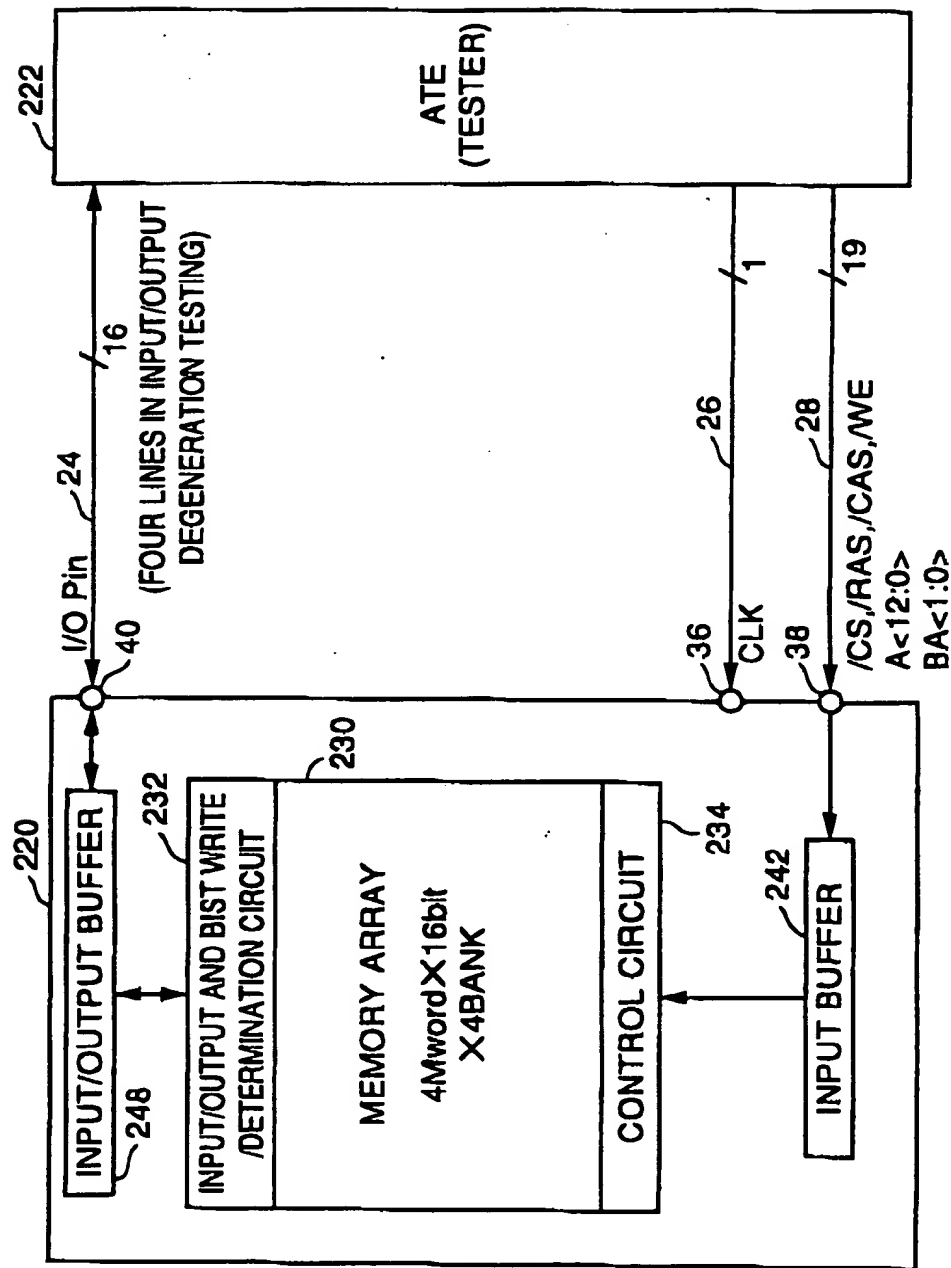


FIG. 1 PRIOR ART



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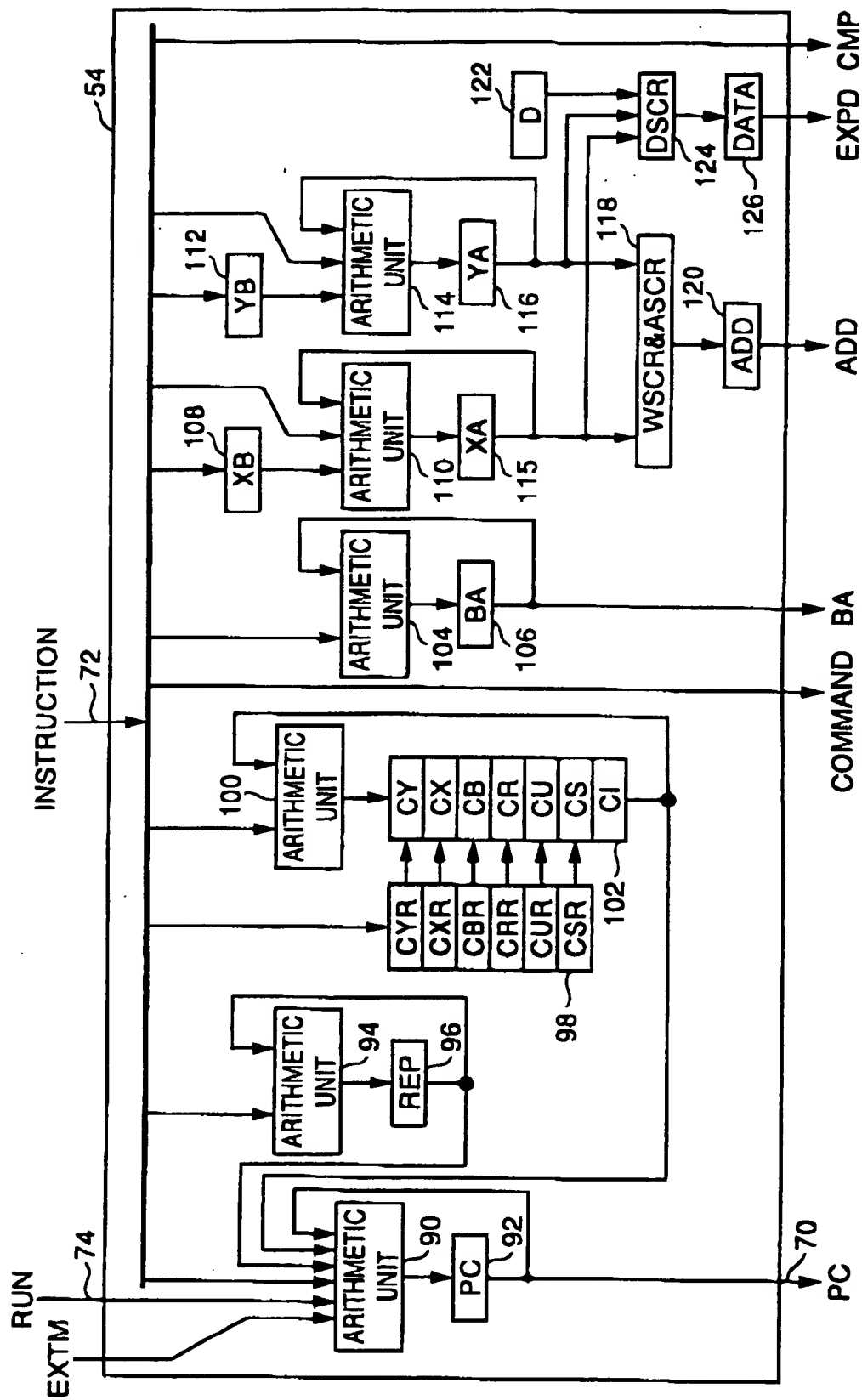


FIG. 4

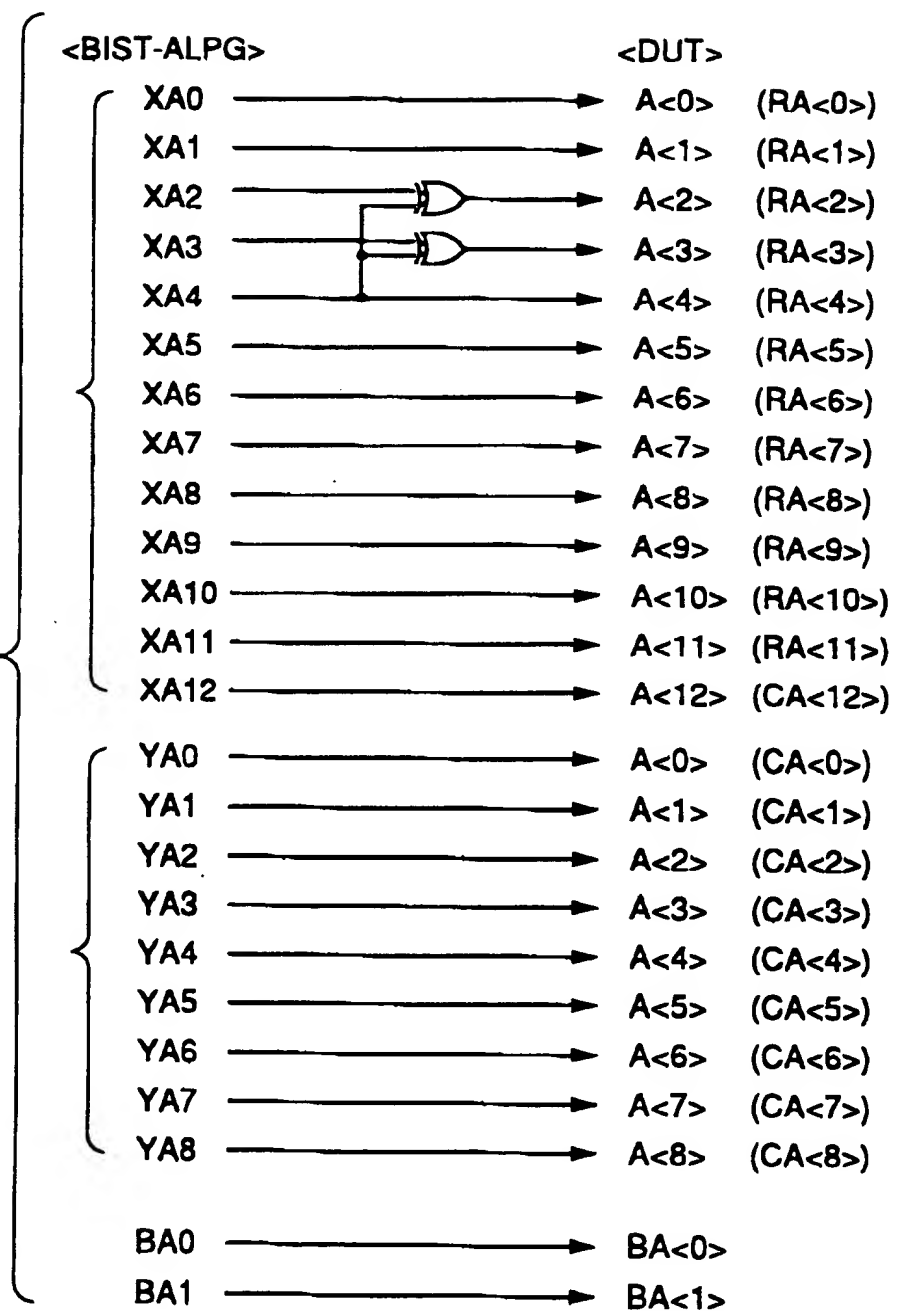


FIG. 5

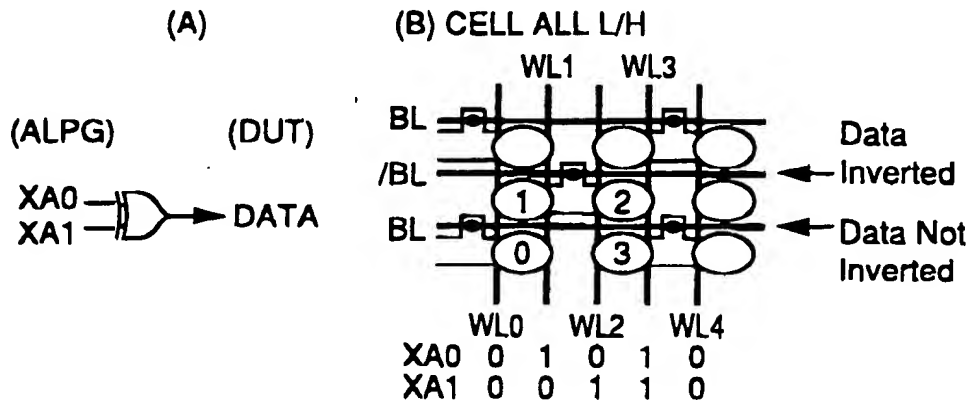


FIG. 6

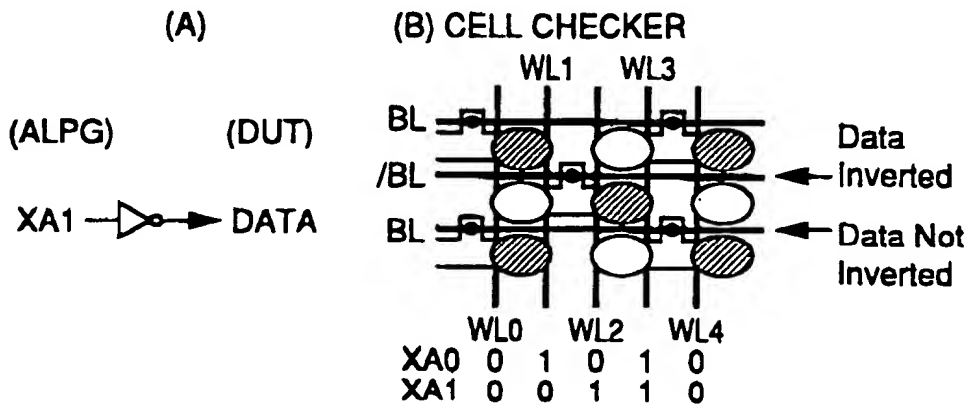
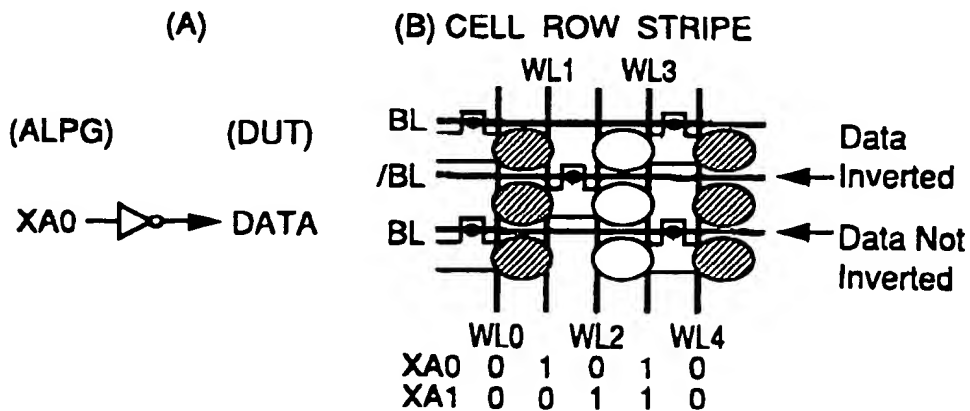


FIG. 7



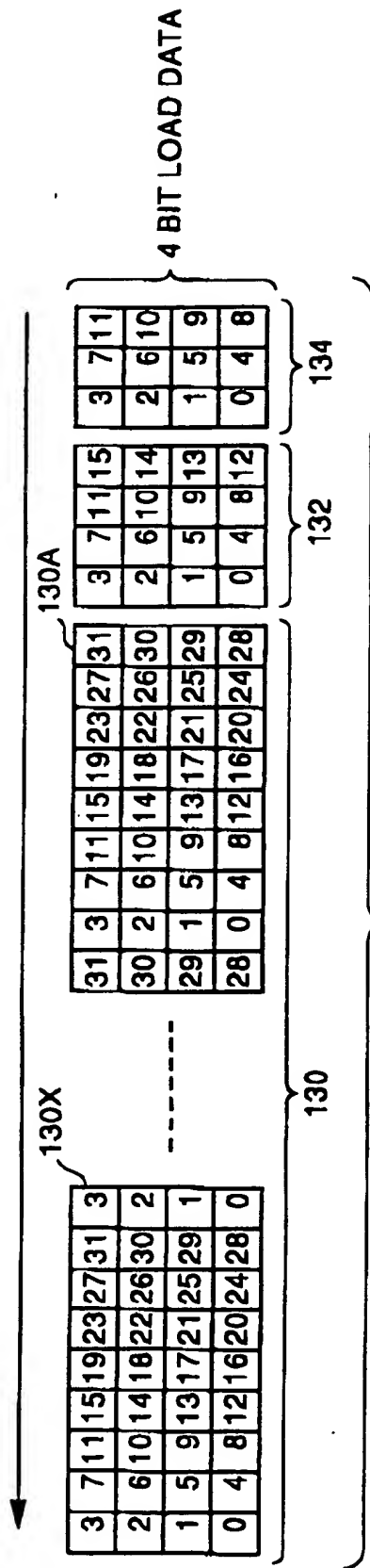


FIG. 8

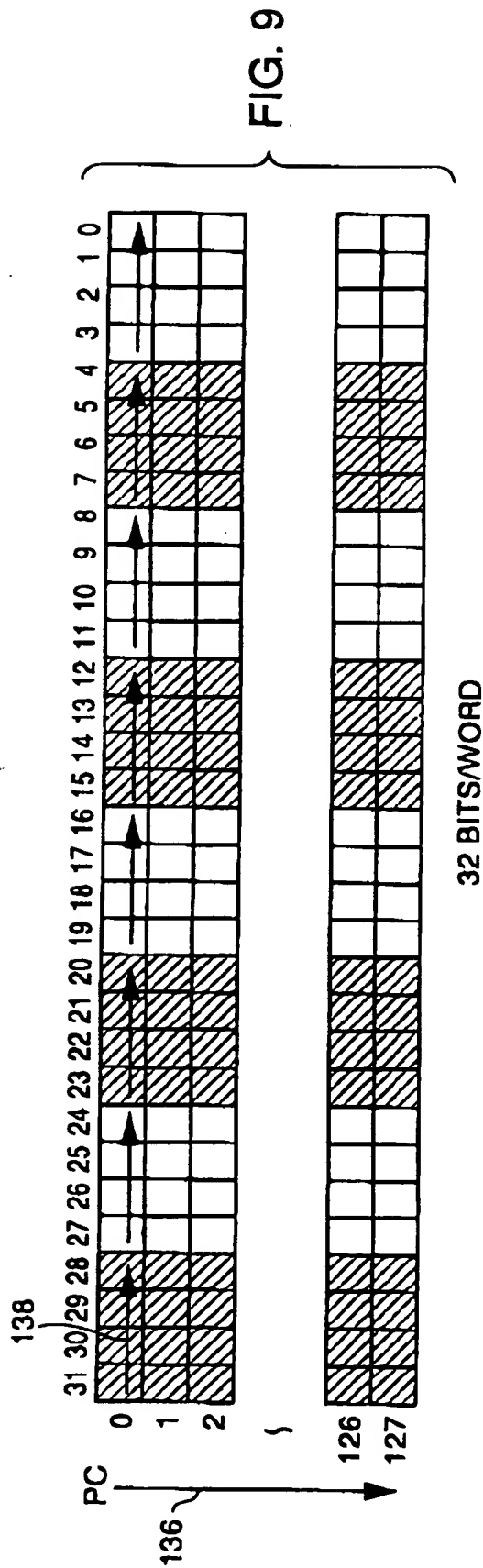


FIG. 10

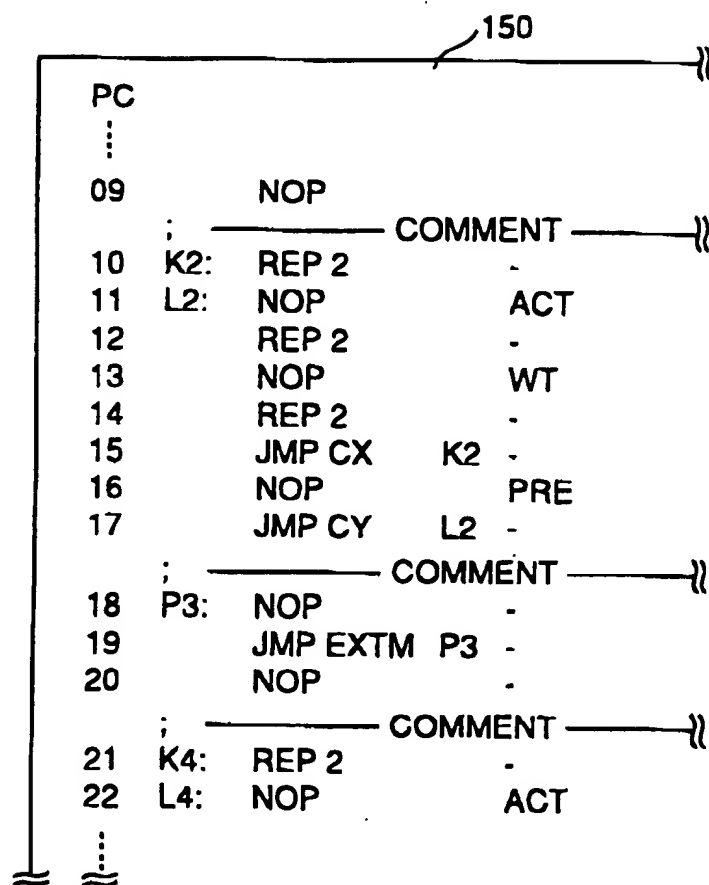


FIG. 11

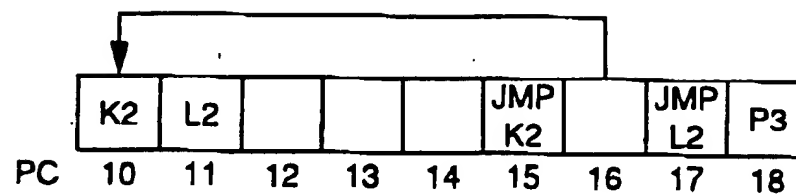


FIG. 12

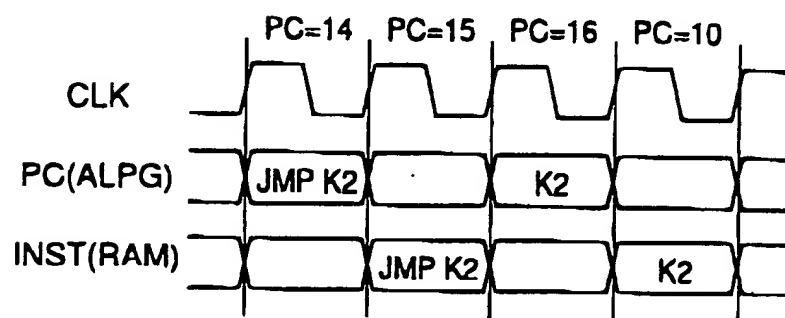


FIG. 13

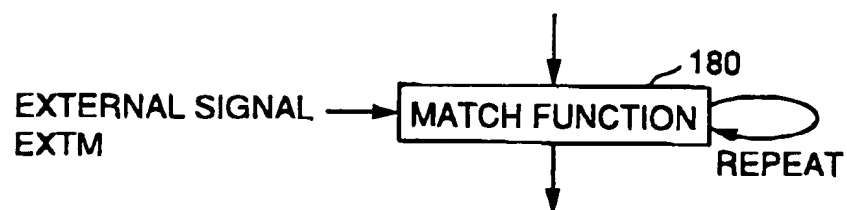
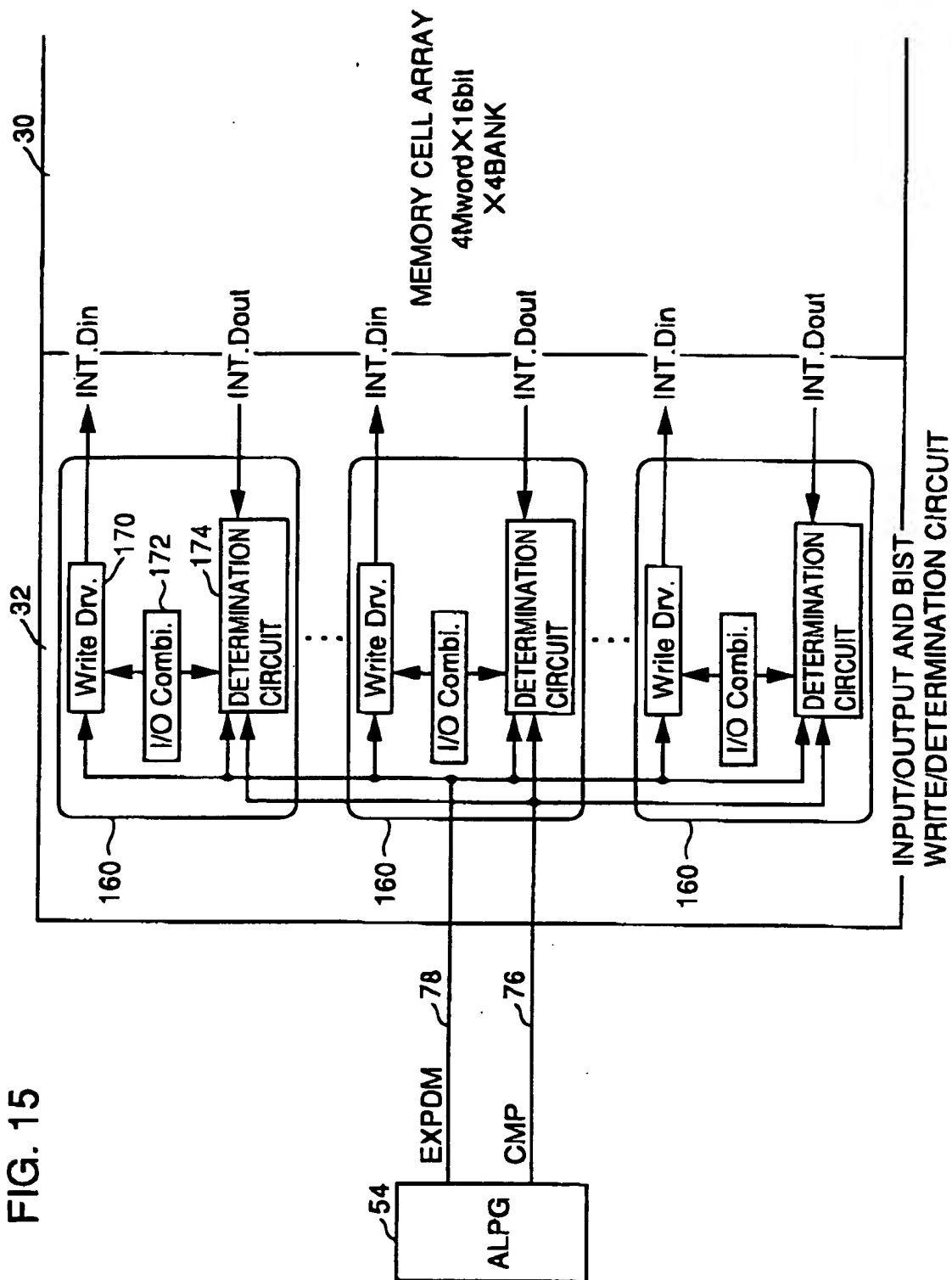


FIG. 15



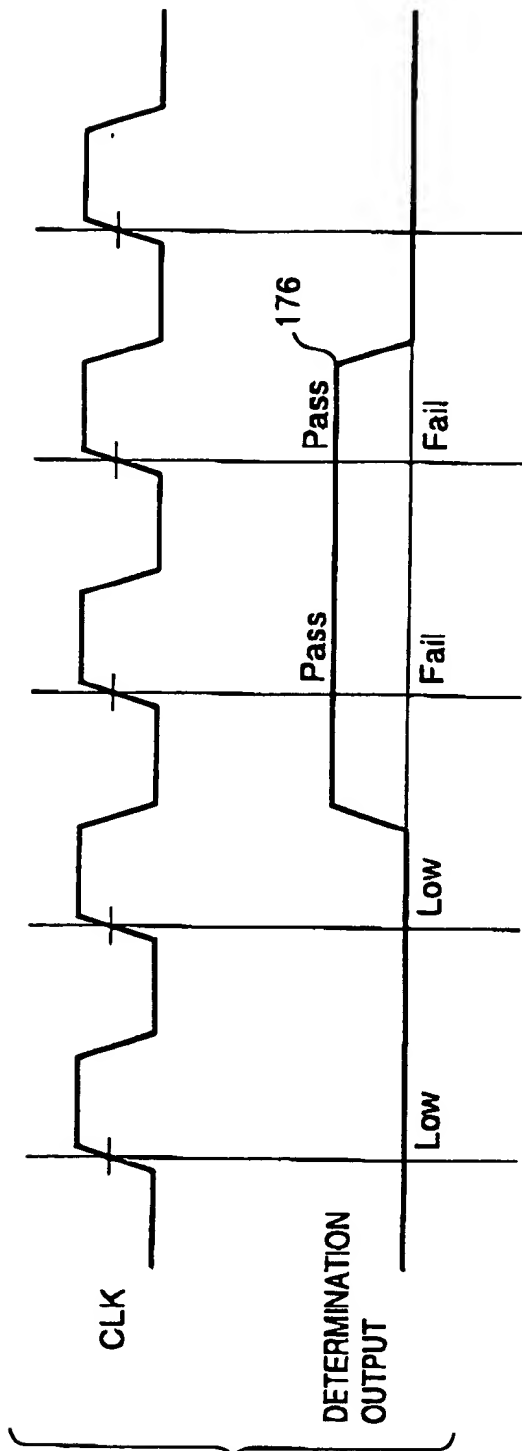


FIG. 16

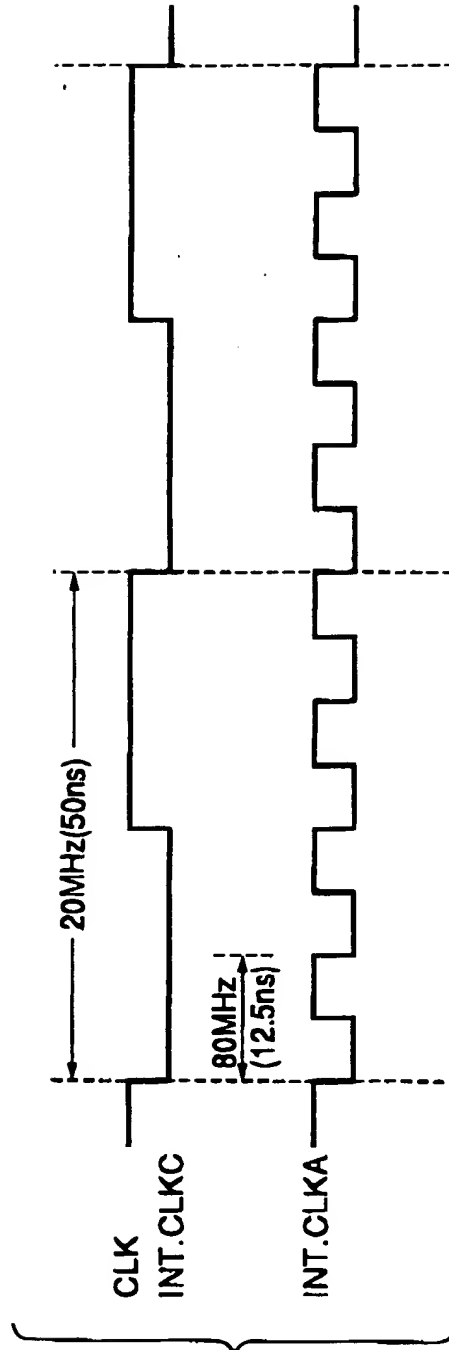


FIG. 18

<CASE-1:Column First Address Increment>

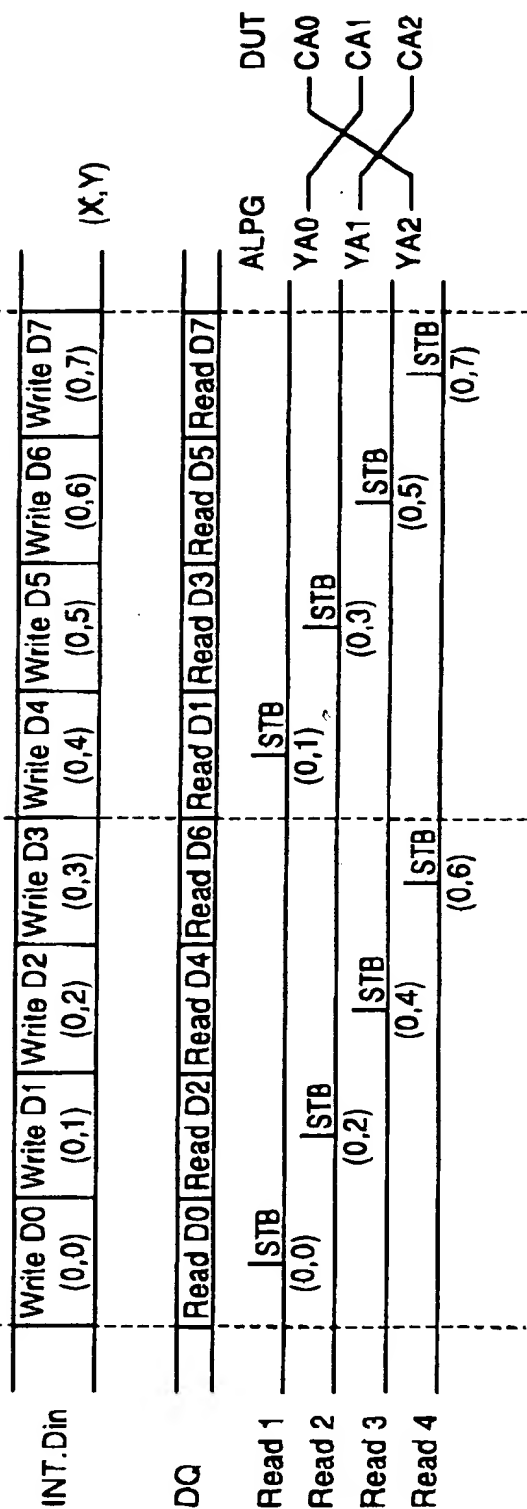


FIG. 19

<CASE-2: Row First Address Increment, BL=4>

INT.Din	Write D0	Write D1	Write D2	Write D3	Write D4	Write D5	Write D6	Write D7
	(0,0)	(0,1)	(0,2)	(0,3)	(1,0)	(1,1)	(1,2)	(1,3)
DQ	Read D0	Read D2	Read D4	Read D6	Read D1	Read D3	Read D5	Read D7
	STB				STB			
Read 1	(0,0)	STB			(0,1)	STB		
Read 2		(0,2)	STB			(0,3)	STB	
Read 3			(1,0)	STB			(1,1)	STB
Read 4				(1,2)	STB			(1,3)

FIG. 20

